

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	50326	sram	US-PGPUB; USPAT	OR	ON	2006/04/27 09:47
L2	10285	sleep adj mode	US-PGPUB; USPAT	OR	ON	2006/04/27 09:48
L3	1013	1 and 2	US-PGPUB; USPAT	OR	ON	2006/04/27 09:48
L4	51663	"365"/\$.ccls.	US-PGPUB; USPAT	OR	ON	2006/04/27 09:48
L5	169	3 and 4	US-PGPUB; USPAT	OR	ON	2006/04/27 09:48
L6	204	houston-theodore-w\$.in.	US-PGPUB; USPAT	OR	ON	2006/04/27 09:49

Day : Thursday  
Date : 4/27/2006

Time: 09:51:46

 **PALM INTRANET**

## Inventor Name Search Result

Your Search was:

Last Name = HOUSTON

First Name = THEODORE

Application#	Patent#	Status	Date Filed	Title	Inventor Name
<u>09069569</u>	6118161	150	04/29/1998	SELF-ALIGNED TRENCHED-CHANNEL LATERAL-CURRENT-FLOW TRANSISTOR	HOUSTON, THEODORE
<u>60011988</u>	Not Issued	159	02/21/1996	REDUCING NON-UNIFORMITY IN A REFILL LAYER THICKNESS FOR A SEMICONDUCTOR DEVICE	HOUSTON, THEODORE
<u>60172058</u>	Not Issued	159	12/23/1999	DYNAMIC THRESHOLD VOLTAGE 6T SRAM CELL	HOUSTON, THEODORE S.
<u>60173042</u>	Not Issued	159	12/23/1999	DYNAMIC THRESHOLD VOLTAGE 4T SRAM CELL	HOUSTON, THEODORE S.
<u>09491942</u>	6362117	150	01/26/2000	METHOD OF MAKING INTEGRATED CIRCUIT WITH CLOSELY SPACED COMPONENTS	HOUSTON, THEODORE W
<u>60259400</u>	Not Issued	159	12/30/2000	Means for forming sol	HOUSTON, THEODORE W
<u>09428835</u>	Not Issued	161	10/28/1999	LOCAL INTERCONNECT STRUCTURES AND METHODS	HOUSTON, THEODORE W
<u>09589951</u>	6207511	150	06/08/2000	Self-aligned trench channel lateral-current-flow transistor	HOUSTON, THEODORE W.
<u>09661659</u>	6376344	150	09/14/2000	SEMICONDUCTOR DEVICE WITH FULLY SELF-ALIGNED LOCAL INTERCONNECTS, AND METHOD FOR FABRICATING THE DEVICE	HOUSTON, THEODORE W.
<u>09661735</u>	6737347	150	09/14/2000	SEMICONDUCTOR DEVICE WITH FULLY SELF-ALIGNED LOCAL INTERCONNECTS, AND METHOD FOR FABRICATING THE DEVICE	HOUSTON, THEODORE W.

<u>09685038</u>	<u>6512272</u>	150	10/10/2000	INCREASED GATE TO BODY COUPLING AND APPLICATION TO DRAM AND DYNAMIC CIRCUITS	HOUSTON, THEODORE W.
<u>09697922</u>	<u>6362058</u>	150	10/26/2000	Method for controlling an implant profile in the channel of a transistor	HOUSTON, THEODORE W.
<u>09727197</u>	Not Issued	161	11/30/2000	METHOD FOR MANUFACTURING A HIGH-FREQUENCY INTEGRATED CIRCUIT FOR REDUCING CROSS-TALK AND FACILITATING ENERGY STORAGE	HOUSTON, THEODORE W.
<u>09877534</u>	<u>6552923</u>	150	06/08/2001	SRAM WITH WRITE-BACK ON READ	HOUSTON, THEODORE W.
<u>09921168</u>	<u>6826730</u>	150	08/02/2001	SYSTEM AND METHOD FOR CONTROLLING CURRENT IN AN INTEGRATED CIRCUIT	HOUSTON, THEODORE W.
<u>09961213</u>	<u>6569741</u>	150	09/21/2001	HYDROGEN ANNEAL BEFORE GATE OXIDATION	HOUSTON, THEODORE W.
<u>09976983</u>	<u>6731533</u>	150	10/12/2001	LOADLESS 4T SRAM CELL WITH PMOS DRIVERS	HOUSTON, THEODORE W.
<u>09997120</u>	<u>6576519</u>	150	11/28/2001	METHOD AND APPARATUS FOR FABRICATING SELF-ALIGNED CONTACTS IN AN INTEGRATED CIRCUIT	HOUSTON, THEODORE W.
<u>09999030</u>	<u>6597610</u>	150	10/25/2001	SYSTEM AND METHOD FOR PROVIDING STABILITY FOR A LOW POWER STATIC RANDOM ACCESS MEMORY CELL	HOUSTON, THEODORE W.
<u>10001473</u>	<u>6635550</u>	150	11/01/2001	SEMICONDUCTOR ON INSULATOR DEVICE ARCHITECTURE AND METHOD OF CONSTRUCTION	HOUSTON, THEODORE W.
<u>10001707</u>	Not Issued	19	10/25/2001	Method and apparatus for reducing capacitive coupling between lines in an integrated circuit	HOUSTON, THEODORE W.
<u>10005863</u>	<u>6461933</u>	150	11/08/2001	SPIMOX/SIMOX COMBINATION WITH ITOX OPTION	HOUSTON, THEODORE W.
<u>10007839</u>	Not Issued	93	11/08/2001	MEANS FOR FORMING SOI	HOUSTON, THEODORE W.
<u>10008690</u>	<u>6576957</u>	150	11/08/2001	ETCH-STOPPED SOI BACK-	HOUSTON,

				GATE CONTACT	THEODORE W.
<u>10010653</u>	<u>6972448</u>	150	11/08/2001	SUB-LITHOGRAPHICS OPENING FOR BACK CONTACT OR BACK GATE	HOUSTON, THEODORE W.
<u>10028199</u>	<u>6687145</u>	150	12/20/2001	STATIC RANDOM ACCESS MEMORY CELL AND METHOD	HOUSTON, THEODORE W.
<u>10028484</u>	Not Issued	168	12/20/2001	4T memory with boost of stored voltage between standby and active	HOUSTON, THEODORE W.
<u>10029494</u>	Not Issued	160	12/20/2001	Refresh at beginning of 4T active cycle	HOUSTON, THEODORE W.
<u>10035923</u>	<u>6483739</u>	150	12/31/2001	4T MEMORY WITH BOOST OF STORED VOLTAGE BETWEEN STANDBY AND ACTIVE	HOUSTON, THEODORE W.
<u>10036095</u>	Not Issued	161	12/31/2001	Partial trench body ties in sram cell	HOUSTON, THEODORE W.
<u>10036296</u>	<u>6639826</u>	150	12/31/2001	MEMORY CELL OPERATION USING RAMPED WORDLINES	HOUSTON, THEODORE W.
<u>10036322</u>	Not Issued	161	12/31/2001	Body-tied-to-source with partial trench	HOUSTON, THEODORE W.
<u>10036324</u>	<u>6628540</u>	150	12/31/2001	BIAS CELL FOR FOUR TRANSISTOR (4T) SRAM OPERATION	HOUSTON, THEODORE W.
<u>10039124</u>	<u>6768144</u>	150	12/31/2001	METHOD AND APPARATUS FOR REDUCING LEAKAGE CURRENT IN AN SRAM ARRAY	HOUSTON, THEODORE W.
<u>10039810</u>	<u>6563158</u>	150	11/16/2001	METHOD AND APPARATUS FOR VOLTAGE STIFFENING IN AN INTEGRATED CIRCUIT	HOUSTON, THEODORE W.
<u>10054957</u>	Not Issued	128	01/25/2002	Asymmetrical devices for short gate length performance with disposable sidewall	HOUSTON, THEODORE W.
<u>10061540</u>	Not Issued	161	10/25/2001	Stacked vias and method	HOUSTON, THEODORE W.
<u>10085646</u>	<u>6750543</u>	150	02/27/2002	SEMICONDUCTOR DEVICE WITH FULLY SELF-ALIGNED LOCAL INTERCONNECTS, AND METHOD FOR FABRICATING THE DEVICE	HOUSTON, THEODORE W.
<u>10140418</u>	Not Issued	161	05/06/2002	Selectively increased interlevel capacitance	HOUSTON, THEODORE W.
<u>10142336</u>	<u>6703673</u>	150	05/09/2002	SOI DRAM HAVING P-DOPED POLY GATE FOR A MEMORY	HOUSTON, THEODORE W.

				PASS TRANSISTOR	
<a href="#">10177773</a>	<a href="#">6573549</a>	150	06/21/2002	DYNAMIC THRESHOLD VOLTAGE 6T SRAM CELL	HOUSTON, THEODORE W.
<a href="#">10178063</a>	Not Issued	160	06/21/2002	Dynamic threshold voltage 4T SRAM cell	HOUSTON, THEODORE W.
<a href="#">10180140</a>	<a href="#">6710391</a>	150	06/26/2002	INTEGRATED DRAM PROCESS/STRUCTURE USING CONTACT PILLARS	HOUSTON, THEODORE W.
<a href="#">10185380</a>	<a href="#">6611451</a>	150	06/28/2002	MEMORY ARRAY AND WORDLINE DRIVER SUPPLY VOLTAGE DIFFERENTIAL IN STANDBY	HOUSTON, THEODORE W.
<a href="#">10227615</a>	<a href="#">6787469</a>	150	08/23/2002	DOUBLE PATTERN AND ETCH OF POLY WITH HARD MASK	HOUSTON, THEODORE W.
<a href="#">10230830</a>	<a href="#">6677190</a>	150	08/29/2002	SELF-ALIGNED BODY CONTACT IN A SEMICONDUCTOR DEVICE	HOUSTON, THEODORE W.
<a href="#">10233352</a>	<a href="#">6734521</a>	150	08/30/2002	INTEGRATED CIRCUIT CELLS	HOUSTON, THEODORE W.
<a href="#">10320222</a>	<a href="#">6735143</a>	150	12/16/2002	SYSTEM FOR REDUCING POWER CONSUMPTION IN MEMORY DEVICES	HOUSTON, THEODORE W.
<a href="#">10324244</a>	Not Issued	161	12/19/2002	Semiconductor apparatus having contacts of multiple heights and method of making same	HOUSTON, THEODORE W.
<a href="#">10337055</a>	<a href="#">6791864</a>	150	01/06/2003	COLUMN VOLTAGE CONTROL FOR WRITE	HOUSTON, THEODORE W.

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<b>Search Another: Inventor</b>	<input type="text" value="houston"/>	<input type="text" value="theodore"/>	<input type="button" value="Search"/>

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